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ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. **UTILITY** Patent Application

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O.I.P.E. *SPD*  
PATENT DATE  
SCANNED *SPD* Q.A. *VT*

APPLICATION NO. 09/732821	CONT/PRIOR D	CLASS 438	SUBCLASS	ART UNIT 2827	EXAMINER
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**TITLE**      **APPLICANTS**

Joseph Fjelstad

## Methods for manufacturing resistors using a sacrificial layer

PTO-2040  
12/99[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Dwg.	Figs. Dwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  _____  _____	_____ (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
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